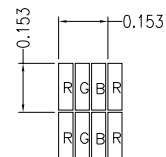
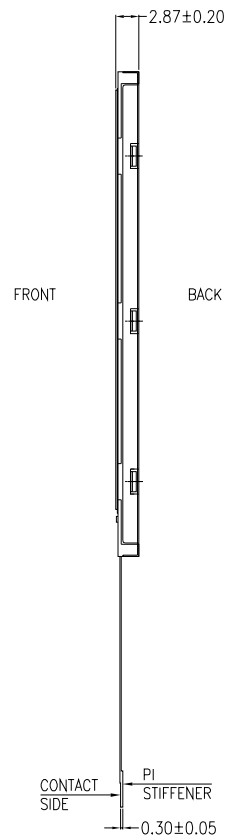
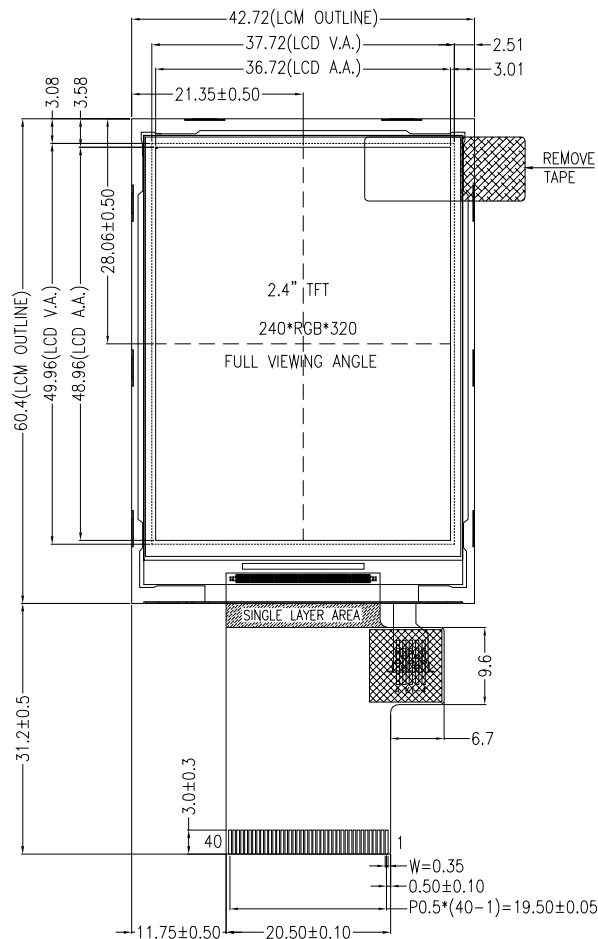


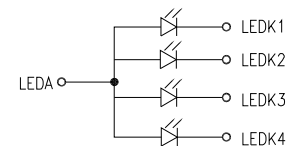
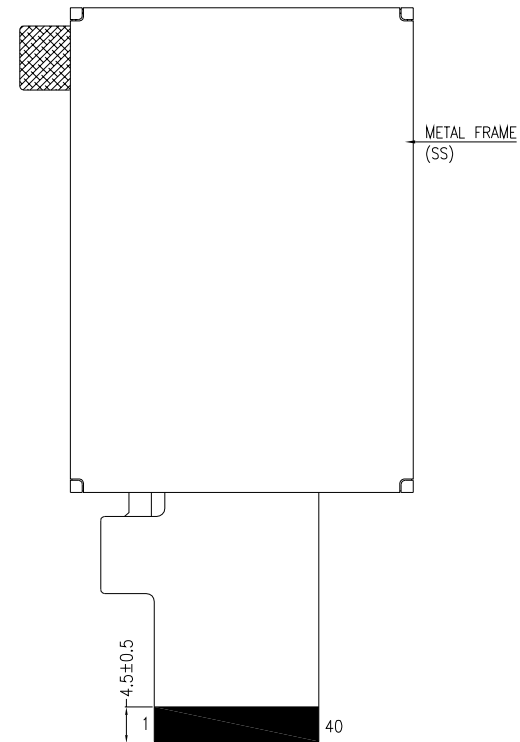
NO MECHANICAL HOUSING TO HOLD THE TFT AND THE BACKLIGHT UNIT ON THIS PRODUCT, SO THE END UNIT'S MECHANICAL DESIGN MUST INCLUDE A HOUSING FOR PREVENTING THE SEPARATION OF THE TFT AND THE BACKLIGHT UNIT.



PIXEL DETAIL
(SCALE 40:1)

IMO	MCU INTERFACE MODE	DATA PIN
0	80-16BIT PARALLEL I/F II	DB [17:10] DB [8:1]
1	80-8BIT PARALLEL I/F II	DB [17:10]

THE MCU INTERFACE MODE SELECT



BACKLIGHT CIRCUIT DIAGRAM

PIN ASSIGNMENT	
PIN	SYMBOL
1	GND
2	NC
3	NC
4	NC
5	NC
6	NC
7	VCC
8	IOVCC
9	FMARK
10	CS
11	RS
12	WR
13	RD
14	DB0
15	DB1
16	DB2
17	DB3
18	DB4
19	DB5
20	DB6
21	DB7
22	DB8
23	DB9
24	DB10
25	DB11
26	DB12
27	DB13
28	DB14
29	DB15
30	RESET
31	IMO
32	NC
33	GND
34	LEDK1
35	LEDK2
36	LEDK3
37	LEDK4
38	LEDA
39	GND
40	NC

NOTES:

- DISPLAY TYPE: TFT, TRANSFLECTIVE, NORMALLY BLACK
- OPERATING VOLTAGE: VCC=2.8V (TYP.), IOVCC=1.8V/2.8V
- VIEWING DIRECTION: FULL VIEWING ANGLE
- DRIVER IC: ST7789T3
- OPERATING TEMP.: -20°C ~ 70°C
- STORAGE TEMP.: -30°C ~ 80°C
- LED BACKLIGHT: WHITE LED, IF=80mA, VF=2.8V~3.4V
- SURFACE LUMINANCE: 100CD/M² (TYP.)
- TOP POLARIZER SURFACE TREATMENT: ANTI-GLARE
- RECOMMEND MATCHING CONNECTOR: HRS FH19SC-40S-0.5SH OR EQUIVALENT
- ONLY THE SINGLE LAYER AREA ON FPC IS BENDABLE AND THE MINIMUM BEND RADIUS IS 1.5MM
- GENERAL TOLERANCE: ±0.2MM
- ROHS COMPLIANT

01	FIRST ISSUE	RITA	2024.05.23	APPROVED BY:		UNIT: mm	DESCRIPTION:
VER.	REVISED DESCRIPTION	REVISER	DATE	VERSION NO:	01	SHEET NO.: 1/1	TFT MODULE

DRAWN BY: RITA		20240523	MODULE P/N:	
CHECKED BY:			MI0240BHT-1	
APPROVED BY:			SCALE : 1/1	
VERSION NO:		01	SHEET NO.: 1/1	